an interposer, the interposer comprising:

a substrate comprised of an electrically insulating ceramic material, the substrate <u>having an outermost surface and</u> being configured for receiving thereon a semiconductive device <u>such that said semiconductive device lies at</u> least in part on said outermost <u>surface</u>; and

a plurality of electrical conductors on the substrate, each electrical conductor having a receiving end for connecting to [a] the semiconductive device and a terminal end for connecting to an electrical apparatus, such that electrical circuitry within the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to said plurality of receiving ends of the electrical conductors and said plurality of terminal ends of the electrical conductors are connected to the electrical apparatus; and

9. (Twice Amended) A system for testing a semiconductive device, the system comprising:

an electrical testing apparatus;

a semiconductive device having an electrical circuitry therein electrically connected to an electrical lead projecting therefrom:

an interposer, the interposer comprising:

a substrate comprised of an electrically insulating material selected from the group consisting of glass, alumina, glass ceramic, nonmetallic nitride, aluminum nitride, nonmetallic carbide, and mixtures and derivatives thereof, the substrate <u>having an outermost surface and</u> being configured for receiving thereon a semiconductive device <u>such that said semiconductive</u> device lies at least in part on said outermost surface; and

an electrical conductor on the substrate, the electrical conductor having a receiving end for connecting to the electrical lead of the semiconductive device and a terminal end for connecting to the electrical testing apparatus, whereby the semiconductive device is electrically coupled to the electrical testing apparatus when the electrical lead of the semiconductive device is in contact with the receiving end of the electrical conductor and the terminal end of the electrical conductor is in electrical communication with the electrical testing apparatus.

an interposer, the interposer comprising:

a substrate comprised of an electrically insulating, ceramic material, the substrate <u>having an outermost surface</u> being configured for receiving thereon a semiconductive device <u>such that said semiconductive device lies at least in part on said outermost surface</u>; and

an electrical conductor on the substrate, the electrical conductor having a receiving end for connecting to the semiconductive device and a terminal end for connecting to the electrical apparatus; and

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet <u>having an outermost</u> <u>surface and</u> comprised of an electrically insulating, inorganic ceramic material, <u>said</u> <u>sheet being configured for receiving thereon a semiconductive device such that said</u> <u>semiconductive device lies at least in part on said outermost surface</u>; and

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to a semiconductive device and a terminal end for connecting to an electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus; and

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet <u>having an outermost</u> <u>surface and</u> composed of an electrically insulating material selected from the group consisting of glass ceramics, devitrified ceramics, vitro ceramics, alumina, single oxide ceramics, and mixed oxide ceramics, and mixtures and derivatives thereof, <u>said</u> <u>sheet being configured for receiving thereon a semiconductive device such that said</u> semiconductive device lies at least in part on <u>said outermost surface</u>; and

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to the semiconductive device and a terminal end for connecting to the electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus; and

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet <u>having an outermost</u> surface and composed of an electrically insulating material selected from the group consisting of alumina, alumina with silica, alumina with silicates, alumina with derivatives of silicates, and mixtures and derivatives thereof, said sheet being configured for receiving thereon a semiconductive device such that said semiconductive device lies at least in part on said outermost surface; and

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to the semiconductive device and a terminal end for connecting to the electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus; and

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet <u>having an outermost</u> <u>surface and</u> composed of an electrically insulating material selected from the group consisting of boron nitrides, aluminum nitrides, and mixtures and derivatives thereof, <u>said sheet being configured for receiving thereon a semiconductor device such that</u> <u>said semiconductive device lies at least in part on said outermost surface</u>; and

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to a semiconductive device and a terminal end for connecting to an electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus; and

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet <u>having an outermost</u> <u>surface and</u> composed of an electrically insulating material selected from the group consisting of oxides of silicon, silicate glass, and nucleated, substantially crystalline glass, and mixtures and derivatives thereof, <u>said sheet being configured for receiving thereon a semiconductive device such that said semiconductive device lies at least in part on <u>said outermost surface</u>; and</u>

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to the semiconductive device and a terminal end for connecting to the electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus; and